

DCI Introduction





ScanWorks®

© 2015, ASSET InterTech, Inc ASSET Confidential

DCI - Background : Basic Problem

- Smaller Intel based products have no space for an XDP connector
- Additionally, it is desirable to be able to debug software problems without removing the case ... Smaller products do not have a way to provide access to an XDP connector with the case intact (Closed Chassis)
- Intel developed the DCI specification to provide solutions in this space (Direct Connection Interface)





© 2015, ASSET InterTech, Inc ASSET Confidential

2

Background: Why CCC

- There is a clear need for Closed Chassis debug
- The Intel DCI specification provides two general methods for this.
 - Direct USB connection from the host to the target
 - Connection from the host to the target via "BSSB"
 - (BSSB is a protocol using USB3 wires but not USB signaling or protocol stack)
- Debug of UEFI requires debug through reset sequences as well as debug around "C-State" changes
- In order to handle reset and C-state changes a Closed Chassis based solution is required when using the DCI interface





3

SourcePointTM Closed Chassis Controller



CCC Details

- Small Probe with USB to host and target
- Implements "BSSB" per the Intel DCI specification
- Supports all BSSB supported debug run-control features available in Intel based targets
- Reset ... Small and inexpensive probe
- Fully compatible with SourcePoint 7.10.4 and beyond
- Delivery estimated Q4 2015



5



Real Insight from Code to Silicon

ASSE





© 2015, ASSET InterTech, Inc., Confidential